

# PA176 - Introduction

Zdenek Matej

Masaryk university

*matej@muni.cz*

February 22, 2024

- Digital computer main parts
- Digital system structure
- Operational memory addressing methods
- Operational and CACHE memory structure, operation principles
- Microprocessor structure, Interruption system principles
- Direct memory access principles
- Digital processing chain
- Digital signal processing methods
- Signal spectra estimation methods
- Analog-digital converters, Digital-analog converters
- Input-output devices
- Power supply unit, switch - mode power supply
- Primary power supply, battery, accumulators

- Lecturers - Matěj, Král + experts from companies
- Final exam - 100 p.
- Minimum - 60 p.

- Study materials will be in IS
- Consultation via email or MS Teams