

Ing. Jiří Čulen 16.03.2023

PA176 Architecture of Digital Systems II



#### Something about me ©



Ing. Jiří Čulen, jiri.culen@mail.muni.cz www.vfnuclear.com.

- Designer of devices for ionizing radiation monitoring on nuclear power plants, nuclear medicine and industry since 2001
- PCB design in Altium Designer + Concorde Pro
- CPLD and FPGA design in Xilinx ISE, Vivado and Vitis





#### **Customer Segments**

**Nuclear Industry** 

Radioactive Waste Management

Medical Industry

**Environmental Monitoring** 

Metrology and Calibration

Dismantling and Decommissioning











#### Area and process monitoring





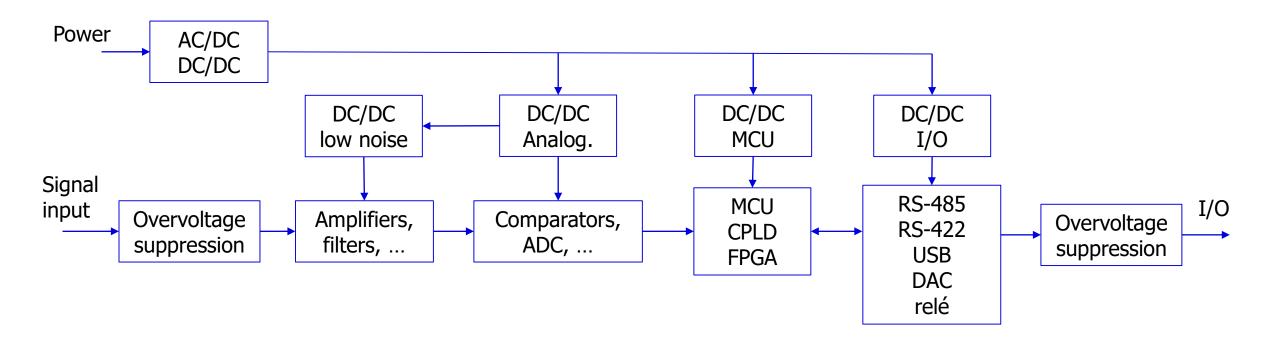
#### **Contamination Monitors**



- Most of the components in PCBs are not implemented due to the main function, but most of it is auxiliary and supporting for main components.
- Typically, more then 50% of this auxiliary components are power supplies.

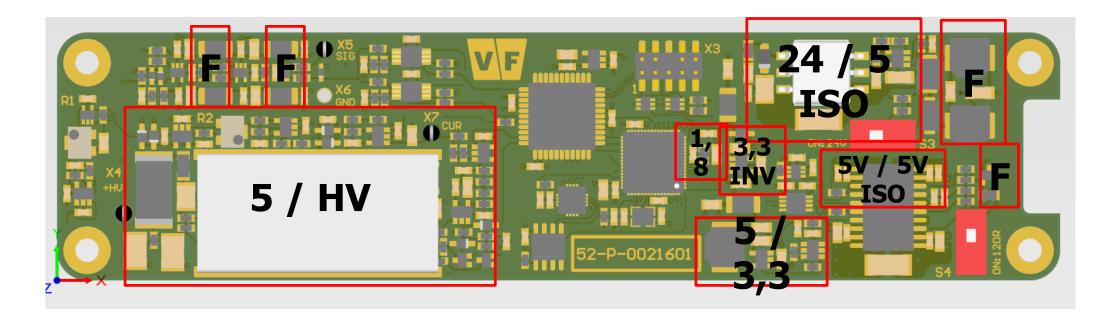


The typical structure of (not only) measuring instruments:





Example of PCB for scintillation detectors:





#### **Linear Power Supply**

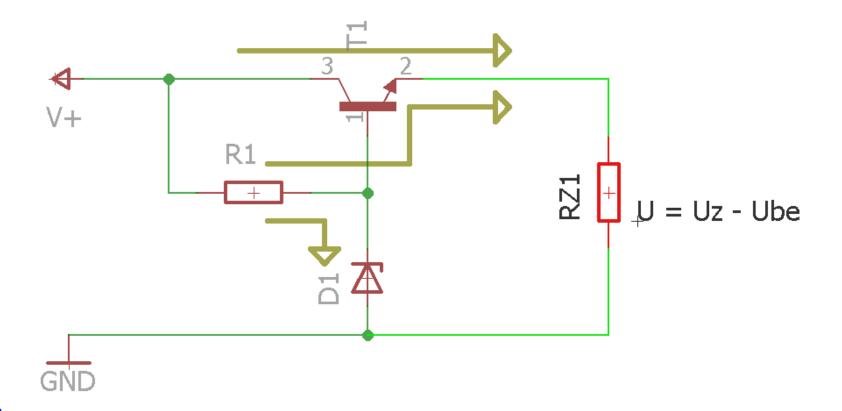
- + low noise
- low efficiency, only for decreasing of voltage

#### **Switching Power Supply**

- + high efficiency, decreasing, increasing, inverting and galvanic isolation of voltage
- high noise

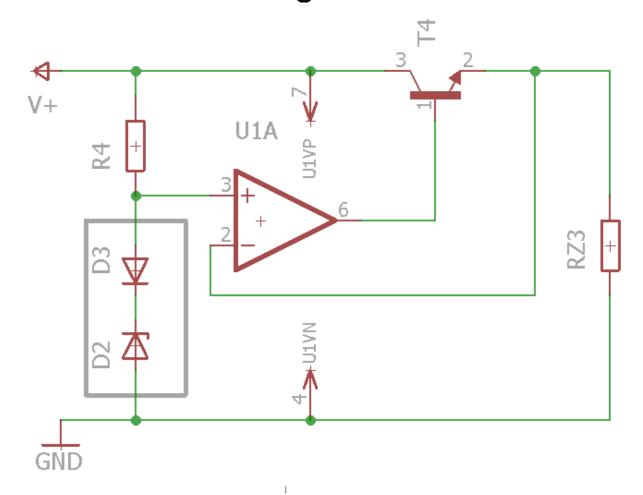


The basic linear voltage regulator



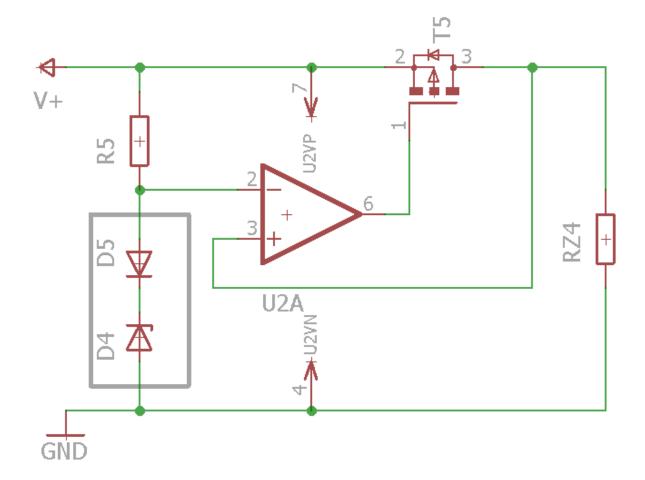


Principial schematic of linear regulators – feedback loop



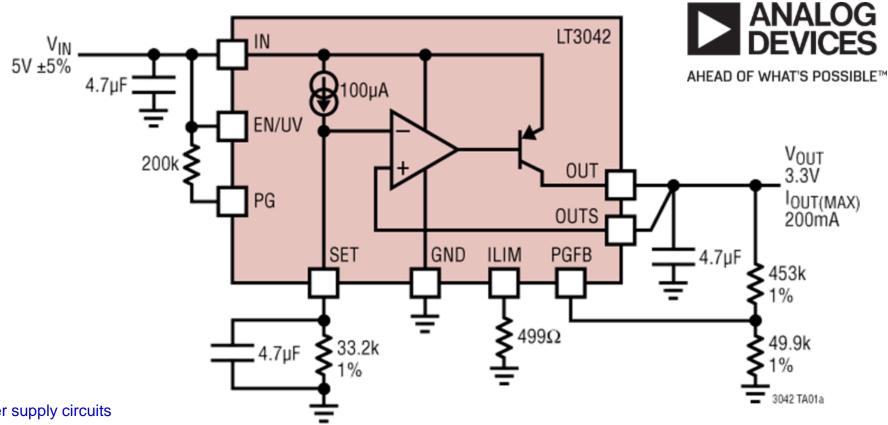


Principial schematic of linear regulators – LDO





Ultralow RMS Noise: 0.8µVRMS (10Hz to 100kHz)

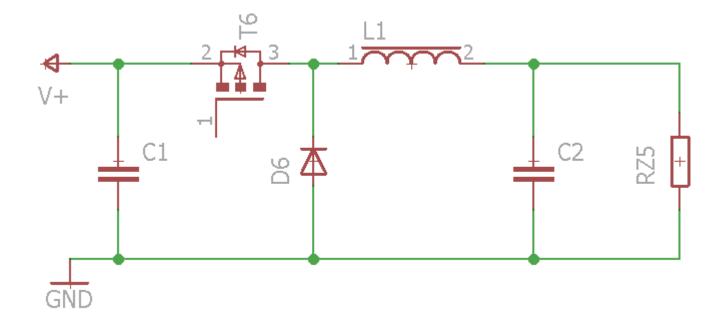




- Most of switching power supply are based on accumulation of energy.
- Accumulation of energy is based on magnetic field (chokes, transformers), on capacitors or on combination of both.

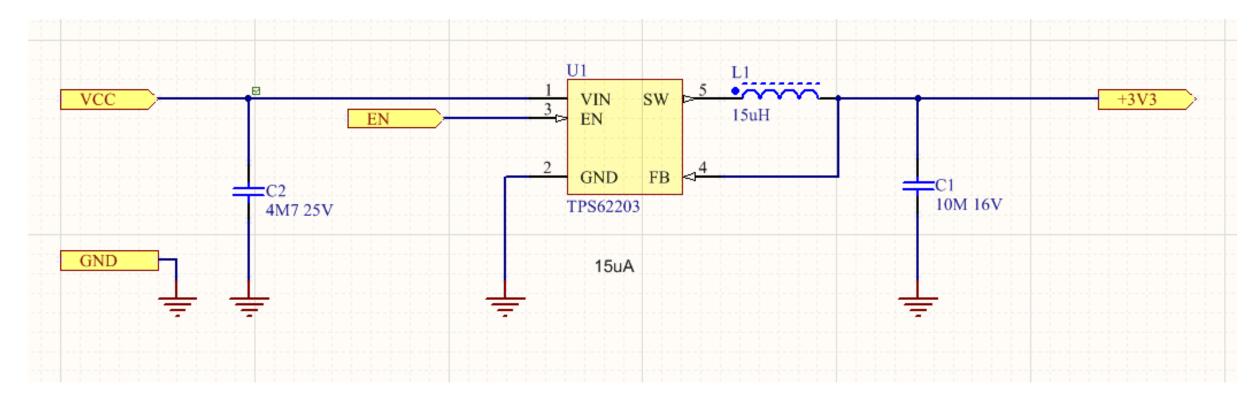


Buck (step down) topology – decreasing of voltage





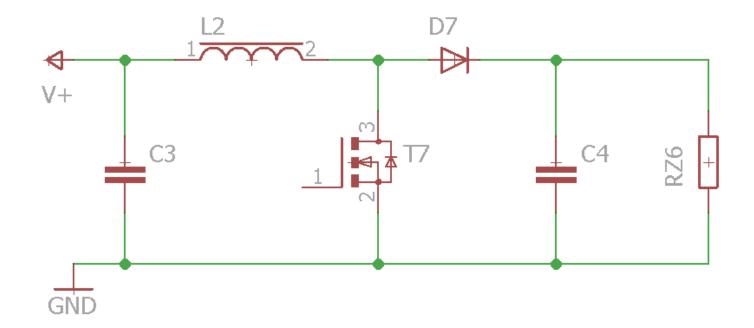
Buck (step down) topology – decreasing of voltage





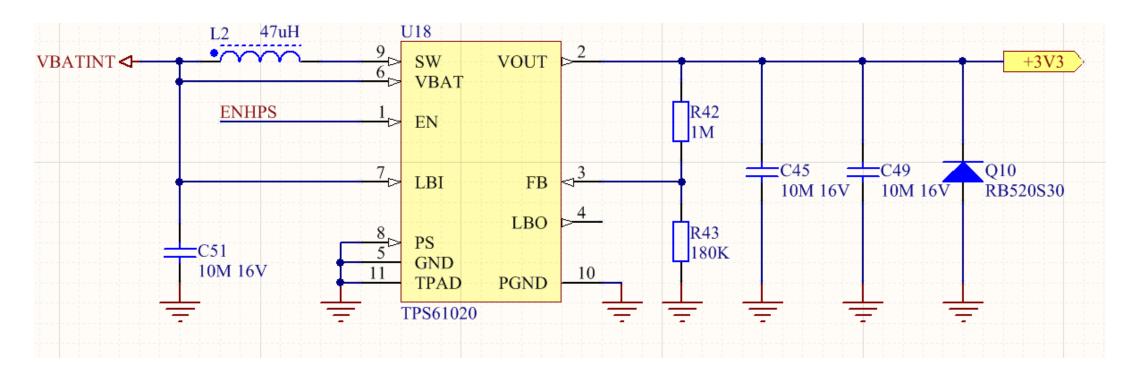
Boost (step up) topology – increasing of voltage

The problem: Not isolating of short circuit



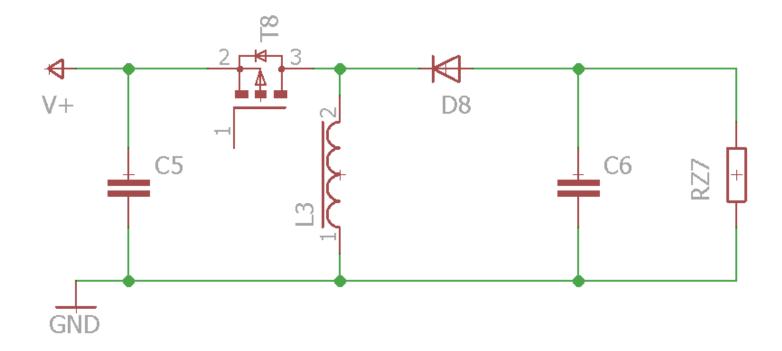


Boost (step up) topology – increasing of voltage





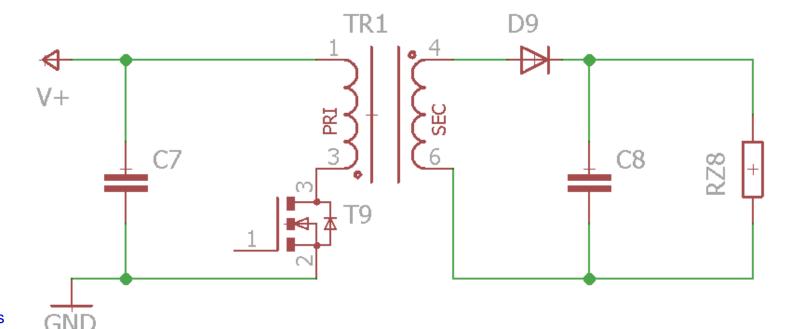
Flyback (inverting) topology – inversion of voltage





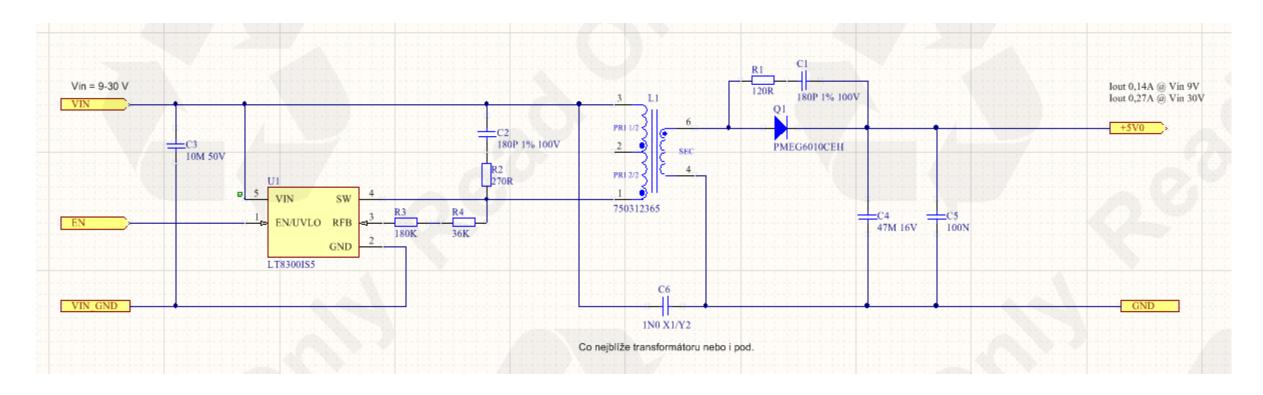
Flyback (inverting) topology – galvanic isolation

- transformer with air gap (difference from pass-through DC/DC)
- up to approximately 200W



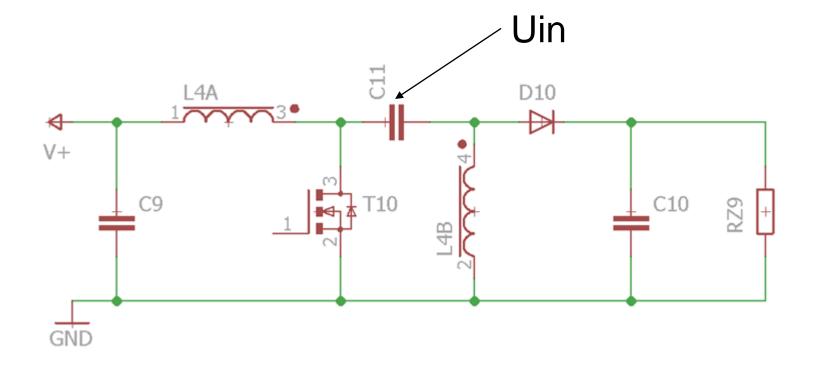


Flyback (inverting) topology – galvanic isolation



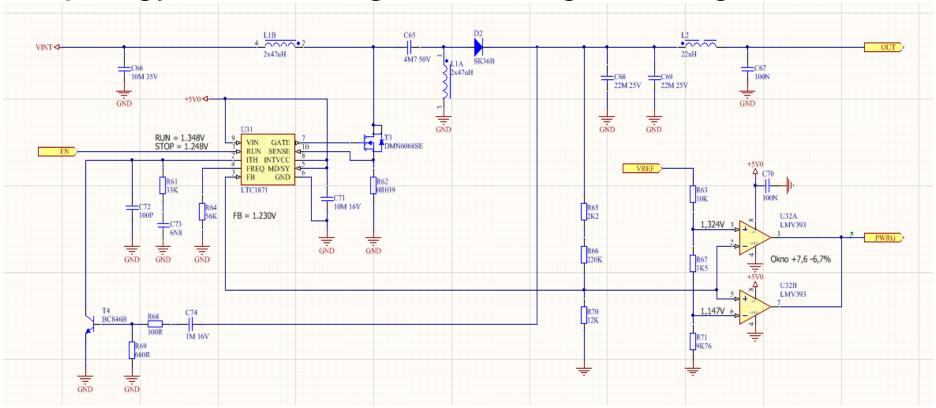


SEPIC topology – increasing/decreasing of voltage



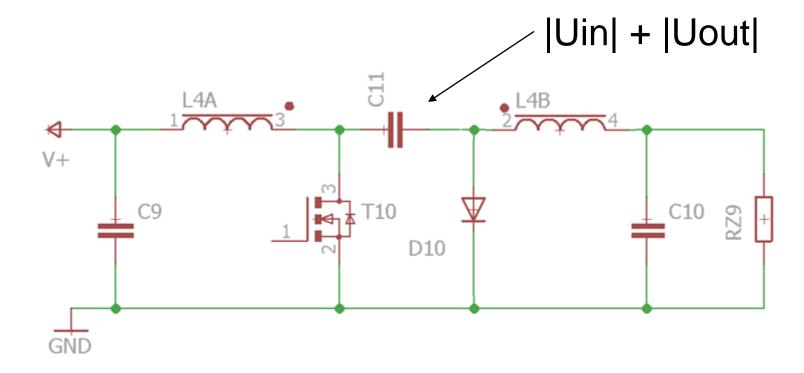


SEPIC topology – increasing/decreasing of voltage



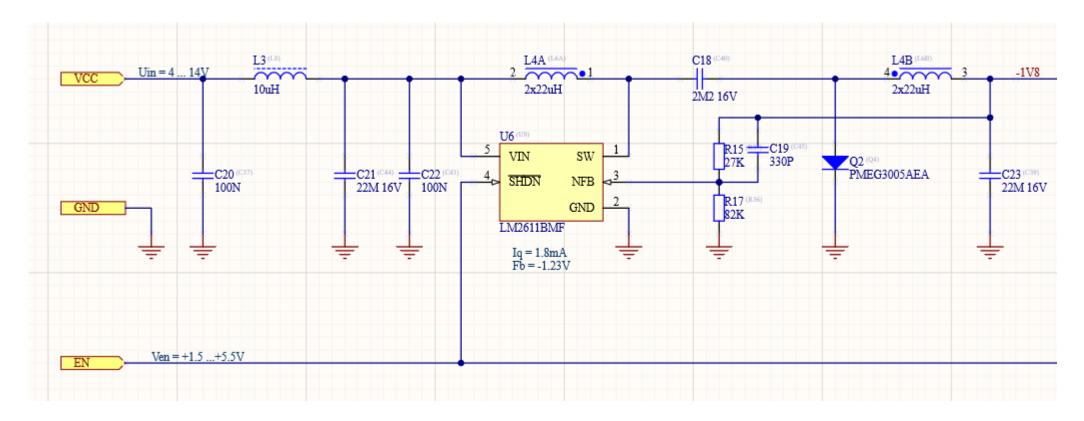


ČUK topology – inversion of voltage



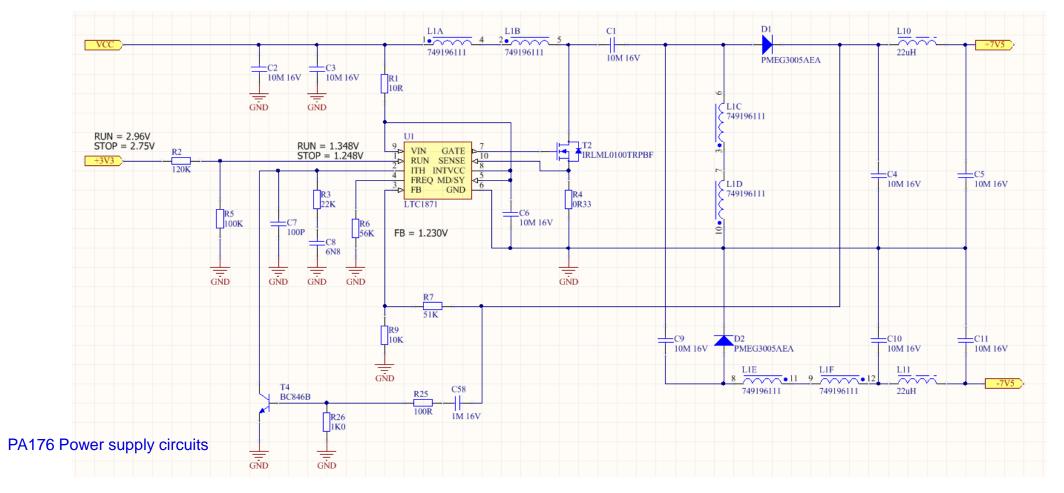


ČUK topology – inversion of voltage





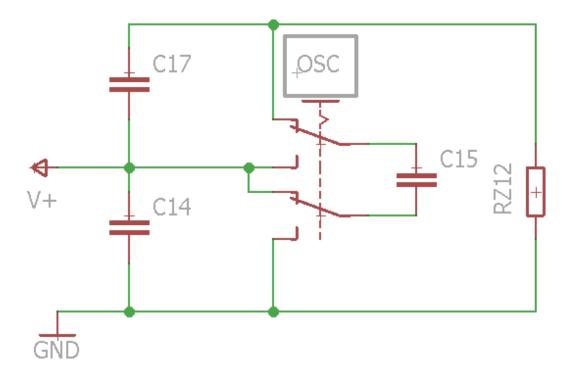
Combination of SEPIC and ČUK topology – symmetric voltage





Charge pump – voltage doubler

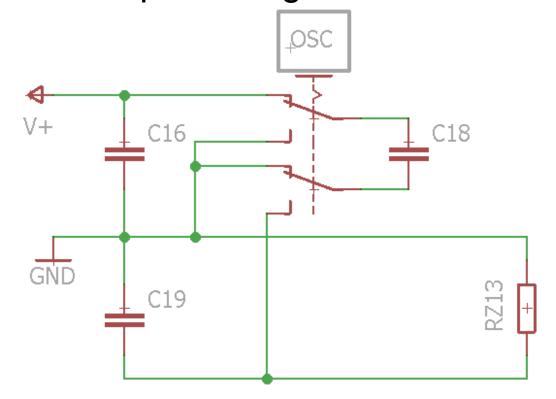
- Very low self consumption
- Not possible to regulate output voltage





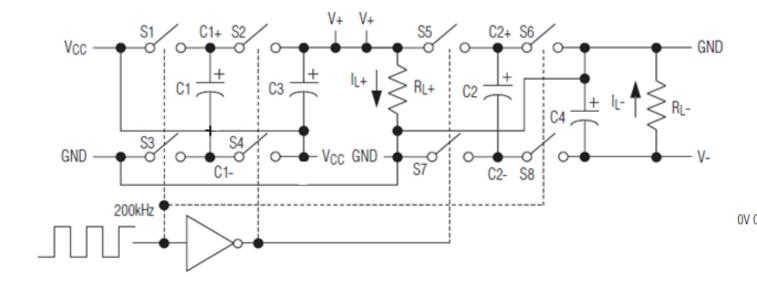
Charge pump – voltage invertor

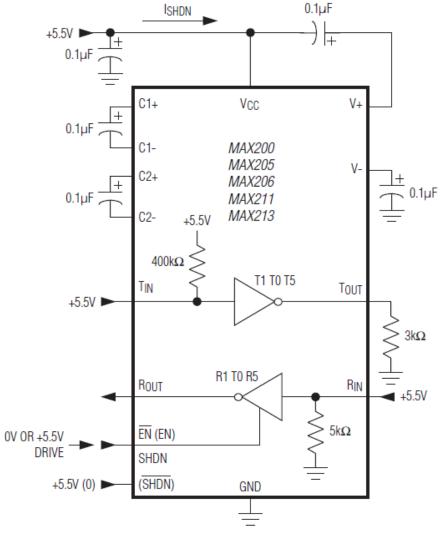
- Very low self consumption
- Not possible to regulate output voltage





#### Charge pump

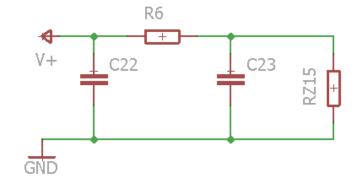


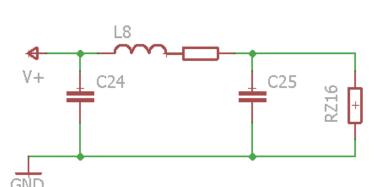


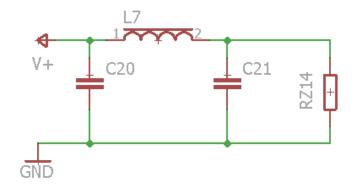


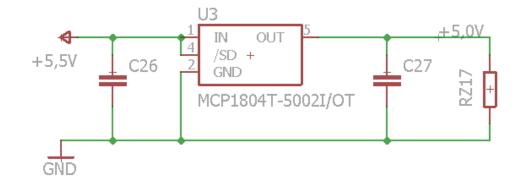
## Filters for power rail

Decreasing of noise and decreasing of crosstalk's





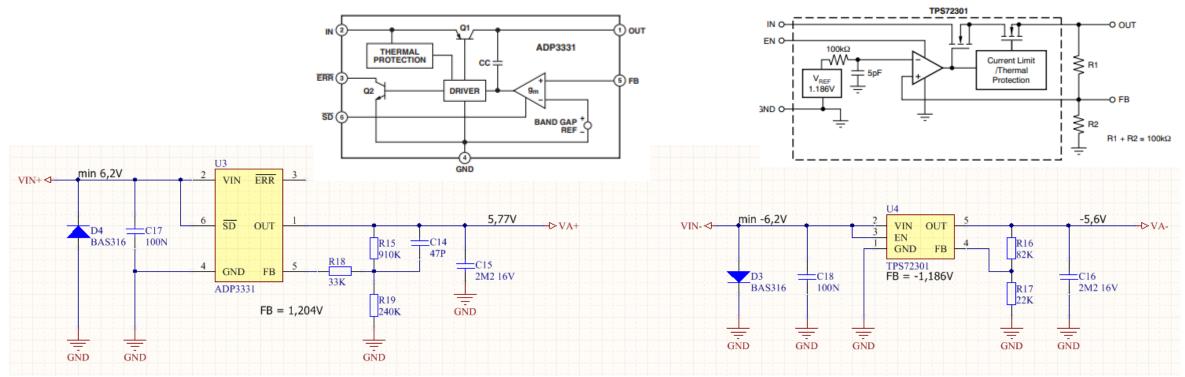






## Filters for power rail

The best filters for low and medium frequency are linear regulators

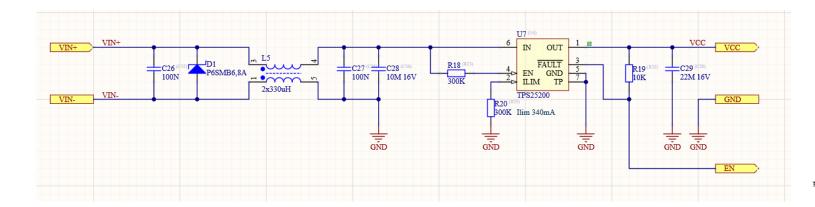


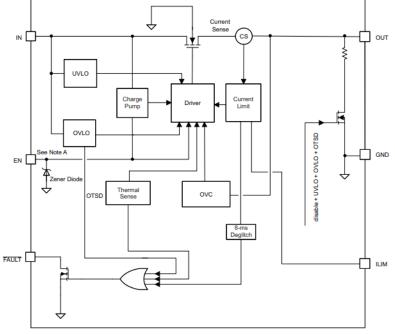


#### Starts of power supplies

It is necessary to be careful about shape, inrush current, rise times

and order of voltages.

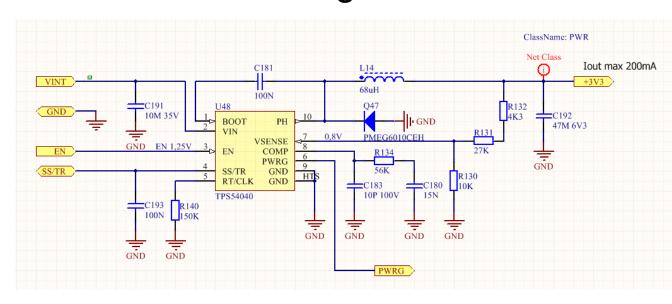






#### Starts of power supplies

It is necessary to be careful about shape, inrush current, rise times and order of voltages.







## **Examples of design in the Altium Designer**







#### References

[1] Texas Instruments, Power Topologies Handbook

[2] Analog Devices, LTspice simulator





# Thank you for attention

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