PV200 HW Introduction to hardware description languages
Autumn 2014

Lector: Martin Veškrna, veskrna@mail.muni.cz

Every Wednesday 10:00 – 11:40 in A415

Taught in English (Czech)

Hardware and language
- Altera DE2 development kit
- Verilog

Content of lessons
- Tutorial (common part, at the beginning)
- Exercises (individual work)

Study materials
- IS

Course completion
- Kolokvium based on the final project
- Attendance: suggested

Final project
- Several theme to choice or you can choice your own
- December 2014 – approval of the theme of the project
- January or February 2015 – defence of the project

Last common lesson
- November 29
- Last three lessons in December – project development