F4280 Technology of Thin Film Deposition & Surface Treatments

Lenka Zajíčková

Faculty of Science, Masaryk University, Brno & Central European Institute of Technology - CEITEC

lenkaz@physics.muni.cz

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1. Field of Expertise, Suggested Literature

What Expertise is Necessary?

Dry material processing requires knowledge of

- ▶ **gas kinetics** (for processes from vapor/gas phase)
- ▶ **film growth** (general views like adsorption, desorption, utilization etc.)
- ▶ **chemical kinetics** (for chemical and plasmachemical methods)
- ▶ **interaction of ions with solid** (for ion beam and plasma techniques)
- ▶ **plasma-related phenomena**, i.e. plasma physics, principles of electrical discharges, elementary processes in plasma, plasma-surface interation

The processes often takes places at decreased pressure. Therefore, a knowledge of **vacuum technology** is also required.

This information are then applied to master the **material processing techniques**:

- ▶ etching (physical sputtering, chemical etching, plasma etching)
- ▶ **vacuum evaporation** for thin film deposition
- ▶ **magnetron sputtering** for thin film deposition
- ▶ **chemical vapor deposition** (CVD)
- ▶ **plasma enhanced chemical vapor deposition** (PECVD)
- ▶ etc.

What is Plasma? How to create it?

Plasma is created from gas by adding sufficient energy (**4th state of matter**). Added energy leads to **ionization of neutral gas**, i. e. generation of electron-ion pairs:

Plasma in thermal equilibrium has extremely high temperature (thermal plasma: fusion, sun)

⇒ many plasmas are created out of thermodynamic equilibrium by increasing **ionization degree** $\alpha = n_i/(n_i + n_g)$ above the equilibrium value with an **additional ionization source**

photoionization (Earth's ionosphere) **electron impact ionization**

(laboratory electrical discharges - electric field accelerates electrons)

Why Plasma in Material Processing?

Low temperature plasma of gaseous discharges provides unique environment for material processing:

- \triangleright hot electrons (T_e few eV, 1 eV = 11 600 K)
	- ⇒ dissociation of molecules into reactive species
- $e^- + AB \longrightarrow A + B + e^-$

- **positive ions** that can be **accelerated** to hundreds of eV near a solid surface (in the **plasma sheath**)
	- ⇒ sputtering of targets, implantation, modification of surfaces and growing films

▶ **cold neutral gas**

⇒ highly energetic process can be kept in a vessel, heat sensitive materials can be treated (e. g. , polymers)

Handbooks of Technologies

- ▶ Handbook of Thin-Film Deposition Processes and Techniques, ed. K. K. Schuegraf, Noyes Publications 1988
- ▶ Handbook of Plasma Processing Technology (Fundametals, Etching, Deposition, and Surface Interaction), ed. S. M. Rossnagel, J. J. Cuomo a W. D. Westwood, Noyes Publications 1989
- ▶ Handbook of Ion Beam Processing Technology (Principles, Deposition, Film Modification and Synthesis), ed. J. J. Cuomo, S. M. Rossnagel, H. R. Kaufman, Noyes Publications 1989
- ▶ Handbook of Plasma Immersion Ion Implantation and Deposition, Wiley 2000
- ▶ Handbook of Thin Film Deposition Techniques (Materials and Processing Technology), by Krishna Seshan, (Noyes Publications 2002)
- ▶ Handbook of Nanotechnology (Springer 2010), B. Bushan

Books Focused on Specific Processes and Technologies

- ▶ Thin Films Phenomena, K. L. Chopra, McGraw-Hill 1969
- ▶ Thin-Film Deposition, Principles and Practice by Donald L. Smith, McGraw-Hill, 1995
- \triangleright Chemical reactor, analysis and design, G. F. Froment and K. B. Bischoff, John Wiley 1990
- ▶ Ion-Solid Interactions, Fundamentals and Applications, M. Nastasi, J. W. Mayer and J. K. Hirvonen, Cambridge University Press 1996
- ▶ Principles of plasma discharges and materials processing, M. A. Lieberman and A. J. Lichtenberg, John Wiley 1994
- ▶ Lecture notes on principles of plasma processing, F. F. Chen and J. P. Chang, Kluwer Academic 2003

Books focused on Specific Materials

- ▶ Tribology of Diamond-like Carbon Films: Fundamentals and Applications, by Christophe Donnet and Ali Erdemir, Springer, 2008
- ▶ Carbon Nanotubes: Science and Applications, M. Meyyappan ed., CRC Press 2004
- ▶ The Science and Technology of Carbon Nanotubes, K. Tanaka, T. Yamabe, F. Fukui eds., Elsevier 1999
- ▶ Nanostructures & Nanomaterials: Synthesis, Properties & Applications by Guozhong Cao, Imperial College Press, 2004

1.2 Overview of Material Processing

Surface Treatment

What can happen after surface treatment?

- \blacktriangleright change of surface roughness
- ▶ change of surface chemistry

What can be these changes used for?

- \blacktriangleright change of surface free energy, i.e. wettability
- \blacktriangleright improved adhesion of further coatings
- \triangleright immobilization of biomolecules

C. Oehr *et al.*, Surf. Coat. Technol. 116–119 (1999) 25–35

Preparation of Films

Difference between thin-film and thick-film technology:

- \blacktriangleright thin-film technology: deposition of individual molecules, film thickness 10 nm–10 μ m
- ▶ thick-film technology: involves deposition of particles (e.g. painting, silk screening, spin-on-glass coating, plasma spraying)

Plasmachemical methods compete with several other approaches on the field of thin film deposition and synthesis of nanostructures

Several aspects have to be taken into account:

- functional properties of the deposition
- uniformity of the processes
- step coverage
- conformality
- reproducibility
- simplicity
- price
- $etc.$

Fig. 8.3a-d Step coverage and conformality: (a) poor step coverage, (b) good step coverage, (c) nonconformal layer, and (d) conformal layer

Etching/Sputtering Processes

ion sputtering

- ▶ purely physical approach, removal by energy transfer
- \blacktriangleright slow process, no selectivity
- ▶ ions are directed by electric field, i.e. anisotropic process

chemical etching

- ▶ purely chemical processes that requires aggressive chemicals and/or elevated temperature for reaction activation
- \blacktriangleright can be very fast, selective
- ▶ chemical reactions with surface are not directed, i.e. isotropic process plasma etching
	- \triangleright combination of physical and chemical approaches
	- directional process

Unique Features of Plasma Technologies

- dry process, i.e. with low consumption of chemicals,
- offering replacement of toxic and explosive reactants
- environmentally friendly
- preparation of new materials

Why? **Plasma** of laboratory electrical discharge provides **environment of**

- ▶ **hot electrons** (*T* ≈ 10000 K) ⇒ dissociation of molecules into reactive species
- **positive ions** that can be accelerated by \approx 100 eV near solid surface \Rightarrow sputtering of targets, implantation, modification of surfaces and growing films
- ▶ **cold neutral gas** ⇒ highly energetic process can be kept in a vessel, heat sensitive materials can be treated (e. g. polymers, even polymer nanofibers)

Plasma Processing Methods

Plasma etching

anisotropic dry etching: combination of chemistry and effect of ions (reactive ion etching)

Plasma deposition of thin films

plasma enhanced chemical vapor deposition (PECVD)

Plasma treatment in O2, NH3, CF4 . . . creation of surface chemical group

physical vapor deposition (PVD) - dc diode sputtering, magnetron sputtering

Commercial Plasma Reactors

Plasma reactors can also look very differently, like plastic boxes :-)

Oxford Instruments, PlasmaPro 100 - **reactive ion etching**

Oxford Instruments, NanoFab

- **high T (plasma enhanced) chemical vapor deposition** for deposition of carbon nanomaterials and other 2D materials

1.3 Introduction to Thin Film Deposition

Thin-Film Deposition Process Steps

All thin-film processes contain the four (or five) sequential steps.

1. A **source** of film material is provided.

Solid, liquid, vapor or gas source. Solid materials need to be vaporized (by heat or energetic beam of electrons, photons, i.e. laser ablation, or positive ions, i.e. sputtering) - **physical vapor deposition (PVD)**. The methods using gases, evaporating liquids or chemically gasified solids are **chemical vapor deposition (CVD)** methods.

2. The material is **transported** to the substrate.

The major issue is uniformity of arrival rate over the substrate area. Transport in a high vacuum $=$ straight travelling lines \rightarrow importance of geometry. Transport in a (gaseous) fluid = many collisions \rightarrow gas flow patterns, diffusion of source molecules through other gases present.

3. The film is **deposited** onto the substrate surface.

It is influenced by source and transport factors and the conditions at the deposition surface. Three principal surface factors: (i) surface condition (roughness, contamination, degree of chemical bonding with the arriving materials and crystallographic parameters in the case of epitaxy), (ii) reactivity of arriving material (sticking coefficient \mathcal{S}_{c} from 1 to less than 10^{−3}) and (iii) energy input (substrate heating, photons, ions, chemical energy).

Thin-Film Deposition Process Steps

- 4. (Optionally, **annealing** takes place)
- 5. The final step is **analysis** of the film.

One level of analysis is the determination of functional properties important for given application and optimization of the deposition process for these processes (emphirical approach). A deeper level of analysis involves probing the film structure and composition (better understanding of the overall processes).

Analysis of the films after deposition - kind of final process monitoring. However, **monitoring** is important in all steps!

Overview of Deposition Methods I

Overview of Deposition Methods I - evaporative methods

vacuum evaporation

pulsed laser deposition

vacuum evaporation (resistive and electron beam

molecular beam epitaxy

Overview of Deposition Methods II

ion beam sputter-deposition

ion-beam assisted deposition (IBAD)

dual ion-beam deposition

1.4 Applications of Thin Films

Overview of Applications Related to Particular Properties

- ▶ Optical properties
	- \blacktriangleright Antireflection coating
	- \blacktriangleright Filters (interference coatings)
	- \triangleright Decoration (color, color effects)
- \blacktriangleright Thermomechanical properties
	- \triangleright Scratch resistant coatings (hardness)
	- \blacktriangleright Thermal protection/heat barriers
	- \blacktriangleright Tribology (friction control, wear resistant films)
- \blacktriangleright (Bio)chemical properties
	- \blacktriangleright Corrosion resistant coatings
	- ▶ Permeation barriers
	- \blacktriangleright Biocompatible surfaces, not-fouling surfaces
- ▶ (Photo)Electrical properties
	- \blacktriangleright Conductors
	- ▶ Insulators
	- ▶ Semiconductor devices (microelectronics)
	- ▶ Photovoltaic materials (sollar cells)
- ▶ Magnetic properties
	- Magnetic storage devices

Thin Films for Optical Applications

$$
r=\frac{r_{12}+r_{23}e^{-i2\Delta}}{1+r_{12}r_{23}e^{-i2\Delta}}
$$

with
$$
n_2d = \frac{\lambda}{4}
$$
 and $\alpha = 0$:

$$
R = \left(\frac{n_1 n_3 - n_2^2}{n_1 n_3 + n_2^2}\right)^2
$$

with
$$
n_1 = 1
$$
 (air) and $n_2 = \sqrt{n_3}$:
\n $\rightarrow R = 0!$

 $\mathcal{O}_{\mathcal{F}^{\text{max}}(\mathcal{F})}$

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Thin Films for Optical Applications

Interference filters and mirrors:

multilayer structure

Important - control of film thickness. roughness (interface) and refractive index

Thin Films for Optical Applications

Interference filters and mirrors: multilayer structure

Important - control of film thickness. roughness (interface) and refractive index

FIG. 2. Refractive index (at λ =550 nm) of different PECVD optical film materials; comparison with selected substrate and PVD materials.

L. Martinu and D. Poitras J. Vac. Sci. Technol. A. Vol. 18.2619

Cutting tools:

Which properties can be improved? What do we achieve with it? What are the challenges?

Hardness - wear resistance, range of materials Friction - wear resistance, cutting speed Thermal stability - cutting speed Heat conductivity - cutting speed Chemical stability - cutting speed and range of materials Color - more attractive for customer

> Challenges: adhesion, cohesion, thermal expansion, chemical stability Complex shape of the object

Thin Films for Mechanical Protection

Challenge - measurement of film properties at thickness < 3 nm

Thin Films for Mechanical Protection

M. Ohring, The Materials Science of Thin Films

Substrate

Thin Films for Mechanical Protection

Alloys can have properties superior to each component

Figure 12-5. Microhardness of mixed carbides due to solid solution and precipitation hardening (From Ref. 3).

M. Ohring, The Materials Science of Thin Films

Multilayer structures can combine properties of different compounds

Thin Films as Barrier Protection

Barrier coatings - permeation barriers

Fig. 5. Permeation rates of 60 nm thick SiO_x films for various pulse powers 600 W $\leq P \leq 3000$ W at plasma conditions $\Phi_{\text{HMDSO}} = 4$ sccm, $\Phi_{\text{O2}} = 400$ sccm, $p=30$ Pa, $t_{on}=4$ ms and $t_{off}=40$ ms.

Thin Films as Barrier Protection

Barrier coatings - permeation barriers

Practical problem: Bottels are filled at pressure $of - 6$ har!

Figure 7. SEM micrographs of SiO_z films on PET after 5 h etching in CCP oxygen plasma.

Important: adhesion, microstructure (defects), elasticity, biocompatibility

1.5 Fabrication of microstructures/microdevices

Microelectronics - Fabrication of Integrated Circuits

Increase of integration:

- Small-Scale Integration (SSI) few transistors on chip,
- Medium-Scale Integr. (MSI) hundreds of transistors on chip (end of 60ties),
- Large-Scale Integration (LSI) 10 000 transistors on chip (70ties),
- ▶ Very Large-Scale Integr. (VLSI) 100 000 transistors on chip (begining of 80ties), 1 000 000 000 in 2007

Microprocessor Transistor Counts 1971-2011 & Moore's Law

Microelectronics - Fabrication of Integrated Circuits

▶ **Front-end-of-line (FEOL)** structure: **complementary metal-oxide-semiconductor** (CMOS) technology is the dominant semiconductor technology for microprocessors, microcontrollers, static RAM and other ICs

CMOS uses complementary and symmetrical pairs of p-type and n-type metal oxide semiconductor field effect transistors (MOSFETs) for logic functions.

Back-end-of-line (BEOL) structure: interconnect metallization, Cu instead of Al and low-k materials are used to decrease the R and C, i.e. BEOL delay.

SEM view of three levels of copper interconnect metallization in IBM's CMOS integrated circuits (Photograph courtesy of IBM Corp., 1997.)

What are MEMS/NEMS?

The acronym MEMS/NEMS (**micro / nanoelectromechanical systems**) originated in the USA. The term commonly used in Europe is microsystem technology (MST), and in Japan it is micro/nanomachines. Another term generally used is micro/nanodevices.

- \blacktriangleright MEMS microscopic devices with characteristic length < 1 mm and > 100 nm
- \triangleright NEMS nanoscopic devices with characteristic length $<$ 100 nm

MEMS/NEMS terms are also **now used in a broad sense** and include electrical, mechanical, fluidic, optical, and/or biological functions. They are referred to as intelligent miniaturized systems comprising e.g. sensing, processing and/or actuating functions.

MEMS/NEMS for

- ▶ optical applications -micro/nanooptoelectromechanical systems (MOEMS/NOEMS),
- ▶ electronic applications radio-frequency-MEMS/NEMS or RF-MEMS/RF-NEMS.
- ▶ biological applications BioMEMS/BioNEMS.

Dimensions of MEMS/NEMS in Perspective

MEMS/NEMS examples shown are of a vertical single-walled carbon nanotube (SWCNT) transistor (5 nm wide and 15 nm high), of molecular dynamic simulations of a carbon-nanotube-based gear, quantum-dot transistor, and digital micromirror device (DMD *http://www.dlp.com*)

Examples of MEMS - gears/motors

- ▶ MEMS motor was developped in lates 1980s using polycrystalline silicon (polysilicon) technology
- left-top photo shows micro-gears fabricated in mid-1990s using a five-level polysilicon surface micromachining technology (J. J. Sniegowski et al. IEEE Solid-St. Sens. Actuat. Workshop, 178–182 (1996)) one of the most advanced surface micromachining fabrication process developed to date
- left-bottom SEM photo microengine output gear and two additional driven gears gear extreme diameter is approximately 50 micrometers and gear thickness is 2.5 micrometers (J. J. Sniegowski et al.)

Approaches in Micro/Nanofabrication

Two principle approaches can be used for micro/nanofabrication:

top-down approach:

- deposition of thin films
- doping
- etching/sputtering (lithography, i.e. through a mask, and nonlitographic fabrication) anisotropic etching of Si
- preparation of surfaces (cleaning, polishing, functionalization)

bottom-up

- ▶ building using nanoobjects (atoms, molecules),
- self-assemply of structures

mohla

Lithography - process flow

Microlithography is a technique that creates microstructures after given geometrical template:

- \blacktriangleright Lithography is usually applied to shape a thin film \Rightarrow deposition of thin film
- \blacktriangleright Photosensitive material (resist) is coated on the material that should be shaped
- \blacktriangleright Resist is irradiated through a mask, by projection of UV image or by directed electrons (photolitography or electron lithography)
- ▶ Resist development:
	- positive resist: soluble in developper at the **irradiated places**
	- negative resits: unsoluble in developper at the irradiated places
- Etching of the film through photoresist pattern
- **Rest of the resist is removed**

lithography patterning with positive resist