C2115 Practical Introduction to Supercomputing

3rd Lesson

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INVESTMENTS IN EDUCATION DEVELOPMENT

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Computer Architecture

CPU, memory, graphics system, disks, network, pheripherials

> Numerical values representation in digital devices

whole numbers, real numbers

From problem to result

algorithm, source codes, compilation, program running, programming languages

Computer architecture

CPU, memory, graphics system, disks, network, pheripherials



Overview

1945 von Neumann architecture



- program may change itself
- program and data can not be loaded simultaneously

John von Neumann, hungarian mathematician, worked in US

1944 Hardward architecture



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Harvard Mark I - completed computer from relay, 24 bit instructions

Overview

1945 von Neumann architecture



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1944 Hardward architecture



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Current computers combine both models.

John von Neumann, hungarian mathematician, worked in US

Harvard Mark I - completed computer from relay, 24 bit instructions

Typical computer scheme



Typical computer scheme, II



CPU

Processor or **CPU** - **Central Processing Unit** is main part of computer; it is complicated sequence circuite that processes **machine code** from operating memory. Machine code consists from machine instructions of computer programs in memory.

www.wikipedia.org



Numerical values representation

> Whole numbers

Real numbers

Whole numbers

Smallest data unit in digital technique is one **bit**. Words are made from bits. Smallest word is **byte**, that contains 8 bits.

One byte may contain numbers from 0 to 255.

Whole numbers with sign may be expressed too. In such a case one bit is dedicated to sign, remaining bits for number. There are multiple possible implementations. Intel architecture uses **two's complement**, that leads to range from -128 to 127.

	128	64	32	16	8	4	2	1		
1	1 0	1	1	1	1	1	1	1	=	127
	0	1	0	1	0	1	1	1	=	87
	0	0	0	0	0	0	0	1	=	1
	0	0	0	0	0	0	0	0	=	0
hit de disete d'feu	1	1	1	1	1	1	1	1	=	-1
bit dedicated for	1	0	1	0	1	0	0	0	=	-87
number sign	1	0	0	0	0	0	0	0	=	-128

Whole numbers, II

Whole numbers with larger range may be expressed by larger words typically consisting of four bytes (32 bit word) or eight bytes (64 bit word).

32 bit whole number unsigned:32 bit whole number signed :64 bit whole number unsigned :64 bit whole number signed :

0 to 4.294.967.295 -2.147.483.648 to 2.147.483.647 0 to 18.446.744.073.709.551.615 -9.223.372.036.854.775.808 to 9.223.372.036.854.775.807

Working with numbers is limited by range, it is **not possible to express any large number**, **avoid overflow** of the value.

Real numbers

Real numbers are expressed in **floating point** format:



In digital technology real numbers are usually expressed according to standard IEEE 754.

type	width	mantissa	exponent
single precision	32	23	8
double precision	64	52	11

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Real numbers, II

type	range	Precision		
single precision	±1,18×10 ⁻³⁸ to ±3, 4×10 ³⁸	Approximately 7 decimal places		
double precision	±2,23×10 ⁻³⁰⁸ to ±1,80×10 ³⁰⁸	Approximately 15 decimal places		

Special combination of mantissa and exponent **special values** may be expressed:

- 0 positive zero
- -0 negative zero
- NaN not a number, e. g. division by zero
- +Inf positive infinity (number too large to express)
- -Inf negative infinity (negative number too large to express)

Working with real numbers is limited by spread of **rounding error**. Logical compating operators **equal to** and **not equal to are not appropriate** to be used with real numbers (except to comparing with zero).

Exercise LII.1

- 1. Variable of **signed char** type(byte with sign) contains number 127. What will be walue if we increment value by one?
- 2. What will be result of sum of two real numbers in double precission with values:

0,1346978.10⁻¹² 1,2312657.10⁶

> Algorithm

- Source codes, compilation
- Running program
- Programming languages









Solving problems using computers (supercomputers) it is necessary to **evaluate** number of **aspects** including used hardware and architecture.



Lecture focus ...

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Programs vs. Scripts

Program is machine instruction file processed directly by processor. It is created by procedure called **compilation** from source code.

Script is text file containing commands and special constructions, these are processed by interpreter of scripting language.



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Programs vs. Scripts, ...

- Easy optimizationFast processing
- Recompilation neededSelf run code not available

 No recompilation
Program can generate and run self running code

Poor optimizationSlower processing



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Programs vs Scripts

Programs dedicated to **demanding scientific-technical calculations** are allways written in compiled programming languages. These include:

- Fortran
- ≻ C/C++

Scripting languages are not used in such calculations at all, or only in supporting parts of calculation, that are not demanding.

Exercise LII.2

1. Determine rate of programs in languages Fortran, C/C++ and others, that are listed on page:

http://en.wikipedia.org/wiki/List_of_quantum_chemistry_and_solid_state_physics_software Plot result in sector graph.